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4914675, Apparatus for efficiently packing data in a buffer, April, 1990  
.... The data stored in the register in the barrel shifter 33 is then divided into ...

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#### [Barrel shifter - Patent 5652718](#)

A barrel shifter performs multi-bit shift and rotate operations on data of ... 4583199, Apparatus for aligning and packing a first operand into a second ...

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### [Letter A Universal Shifter with Packed Data Formats 1 ...](#)

Keywords: Universal shifter, Barrel shifter, SIMD, Packed data, .

Crossbar network ... to execute data reorganization operations such as pack, ...

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The 2D-Compressor generates data that have to be temporarily stored in a FIFO buffer. This is done by means of a barrel shifter that outputs a fixed length ...

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### [Apparatus for packing parallel data words having a variable width ...](#)

An apparatus for packing data, receiving parallel data words arranged in blocks of data, .....

As the bit shifter 30, for example a 32-bit barrel shifter, ...

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### [Circuit and method of bit-packing and bit-unpacking using a barrel ...](#)

Circuit and method of bit-packing and bit-unpacking using a barrel shifter - US Patent

5499382 from Patent Storm. A data compressor generates codewords ...

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Variable length coder and data packing circuit - US Patent 5321398

The data stored in the register in the barrel shifter 33 is then divided ..... Inventor:

Bahgat4914675, Apparatus for efficiently packing data in a buffer ...

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Google Answers: What is a "barrel shift"?

Yes, a barrel shifter can be used to rotate data. ... 32-bit cascadable barrel shifter "designed for use in floating-point normalization, word pack/unpack, ...

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A VME barrel shifter system for event reconstruction for up to 3 ...

of this barrel shifter system are switch modules, optical to ... data flow and the data set is arranged in. a. set of pack-. ets of the same length. ...

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Timing-Driven Decomposition of a Fast Barrel Shifter

length coding, word packing/unpacking, bit indexing, address ... a barrel shifter, if the data input signal is n-bits wide, then the ...

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Often this is accomplished with data packing, which allows registers, data paths, ... a 40-bit barrel shifter, and quad 8-bit ALUs for video operations. ...

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Inventors Yuji Ishikawa, Yokohama (JP) Assignees Canon Kabushiki ...

Control is performed to byte pack the bits of the last byte and the shifted bits outputted from the barrel shifter, and coded data is converted into data in ...

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[focus.ti.com/docs/prod/folders/print/tms320vc5404.html](http://focus.ti.com/docs/prod/folders/print/tms320vc5404.html) - 60k - [Cached](#) - [Similar pages](#)

AES E-Library: A Single-Chip DSP Implementation of a High-Quality ...

These include an optimized FFT kernel, provision for low-overhead block floatin g-point, and a barrel shifter for data packing and envelope information ...

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Stratix II FPGA Design Building Block Performance

A barrel shifter rotates the input data bits by the amount specified by the .... resources usage by packing logic functions and common inputs together. ...

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Details and datasheet on part: DSP56303PV100

Program Add ress Ge nerator 24 × 24 + 56 56-bit MAC Two 56-bit Accumulators 56-bit Barrel Shifter Data ALU Power Management JTAG OnCETM ...

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Design and Implementation of a High-Performance Microprocessor ...

The decompressor then shifts in more compressed data using a barrel shifter and concatenates them with the remaining compressed bits. ...

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Data arithmetic logic unit (Data ALU) with fully pipelined 24 × 24-bit parallel Multiplier-Accumulator (MAC), 56-bit parallel barrel shifter (fast shift ...

[www.freescale.com/files/dsp/doc/data\\_sheet/DSP56311.pdf](http://www.freescale.com/files/dsp/doc/data_sheet/DSP56311.pdf) - [Similar pages](#)

#### [EP405805 Ibm european software patent - Apparatus for processing ...](#)

Data packing of the line buffer (i.e. the RAM) can be performed on a multiple ... Figures 20 - 22 illustrate the modules constituting the barrel shifter; ...

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offer excellent packaging and communication advantages, a soft- .... a barrel shifter to enable the bs and bsi instructions. ...

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Nov 7, 2007 ... This enables shifting, interleaving, packing, and unpacking operations ... BSHF: Barrel SHiFter. This enables shifting operations and can be ...

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At a data packing density of 1.3 bits per mm, this implies that over 53 mm .... a ring or rotating barrel shifter, and rotate them by 35 bits (in steps of ...

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XPP (eXtreme Processing Platform) is a data processing architecture based on a hier- ....

BREG: barrel shifter, packing, unpacking and clipping operators ...

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typically utilize barrel shifter circuitry to allow a plurality of bytes to be shifted by .... reordered and shifted to perform data packing and unpacking. ...

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#### [Texas Instruments' TMS320C40](#)

20-Mbytes/sec asynchronous transfer rate at each port for maximum data ... Single-cycle barrel shifter for 0-31 single-cycle right or left shifts for fast ...

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#### [ARM1136JF-S™ and ARM1136J-S™ Technical Reference Manual 1.2.1. Core](#)

Only load, store, and swap instructions can access data from memory. ... The Shift stage contains the full barrel shifter. All shifts, including those ...

[infocenter.arm.com/help/topic/com.arm.doc.ddi0211j/Cacebjhf.html](http://infocenter.arm.com/help/topic/com.arm.doc.ddi0211j/Cacebjhf.html) - 15k -

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The barrel shifter can shift data up to 16 bits left or right in a single .... packaging and electrical characteristics, the device datasheets listed here ...

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This capability allows programmers to tightly pack data or code and conserve RAM, which is a high-cost item for embedded applications. ...

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